

10/560756

Docket No.: 96790P515

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

YASUHIRO ODA, ET AL.

Application No.:

Filed:

For: **HETEROSTRUCTURE BIPOLAR
TRANSISTOR**

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed), which are being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: _____

12/13/05

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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Substitute for form 1449A&B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>		
				Application Number		10/560756
				Filing Date		
				First Named Inventor		Yasuhiro Oda
				Art Unit		
				Examiner Name		
Sheet	1	of	1	Attorney Docket Number		

U.S.PATENT DOCUMENT					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² <i>(if known)</i>		
		US			
		US			
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FOREIGN PATENT DOCUMENT							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Tranlation ⁶
		Country Code ³	Number ⁴	KindCode ⁵ <i>(if known)</i>			
		JP	2005-086135	A	03-31-2005	NIPPON TELEGR&TELEPH CORP	Abstract
		JP	2004-214576	A	07-29-2004	NIPPON TELEGR&TELEPH CORP	Abstract
		JP	2003-086602	A	03-20-2004	NIPPON TLEGR&TELEPH CORP	Abstract
		JP	2002-270615	A	09-20-2002	AGILENT TECHNOL INC	Abstract
		JP	2002-270616	A	09-20-2002	AGILENT TECHNOL INC	Abstract

OTHER PRIOR ART–NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium catalog, etc.), date, page(s), volume–issue number(s), publisher, city and/or country where published.	Translation ⁶
		M.W.Dvorak et al., “300 GHz InP/GaAsSb/InP Double HBTs with High Current Capability and BVceo ≥ 6V”, IEEE ELECTRON DEVICE LETTERS, VOL.22, NO.8, August 2001, pp361	
		F. Brunner et al., “Growth Monitoring of GaAsSb: C/InP Heterostructure with Reflectance Anisotropy Spectroscopy”, TMS, Abstract of 12 th ICMOVPE, 2004, pp.2	
		Oda et al., “Suppression of hydrogen passivation in carbon-doped GaAsSb grows by MOCVD”, ELSEVIER, Journal of Crystal Growth, Vol.261, 2004, pp.393	

Examiner Signature		Date Considered	
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